

Choice of ALD Precursors for Microelectronics and Nanoelectronics

Roy G. Gordon

Harvard University Chemical Laboratories, Cambridge, MA 02138, USA

Gordon@chemistry.harvard.edu (617)495-4017

We review ALD processes that are applicable to the scaling of microelectronic devices to smaller dimensions, as well as to potential structures for future nanoelectronics. Effective candidate materials are given (in parenthesis) for each of the functions listed below:

High-k dielectric on Si (GdScO_3)

High-k dielectric on Ge (HfO_xN_y)

High-k dielectric on GaAs ($\text{Gd}_x\text{Ga}_{1-x}\text{O}_3$)

High-k dielectric on noble metals (Ta_2O_5)

Thermally stable metal gates (HfN, TaN)

Replacement metal gates (Ru, WN, CoSi_2 , NiSi)

Source/drain contacts (CoSi_2 , NiSi)

Pre-metal dielectric (carbon-doped silica)

Sealing the surfaces of porous low-k dielectrics (carbon-doped silica)

Copper diffusion barriers (WN)

Copper adhesion layers (Ru, Co)

Copper seed layers (Cu)

MRAM (Fe, Co, Ni, Ru, MgO)

Insulation for through-die interconnects (doped SiO_2)

Coaxial gated transistors on single-walled carbon nanotubes (any high-k/metal combination, following non-covalent functionalization with NO_2)

The most effective ALD precursors will be discussed for each of these applications. The considerations for each choice are volatility, thermal stability, reactivity, melting point and scalability of chemical synthesis.

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Roy G. Gordon
Harvard University
Cambridge, MA

Outline

High-k Dielectrics

More Stable Precursors for Zirconium and Hafnium

First Precursors for ALD of GdScO_3

Interconnects

ALD / CVD Ruthenium Glue Layers

ALD Copper Seed Layers

Electroplating on Cu / Ru / WN

Why More Stable Hf and Zr Precursors?

High-k HfO₂ or ZrO₂ with very low electrical leakage is needed

Carbon impurity in films increases leakage

Thermal decomposition of organic precursors adds C to films

Thermal decomposition destroys uniformity and conformality

Deposition T ~ 400 °C needed for HfAlO_x with ALD Al₂O₃

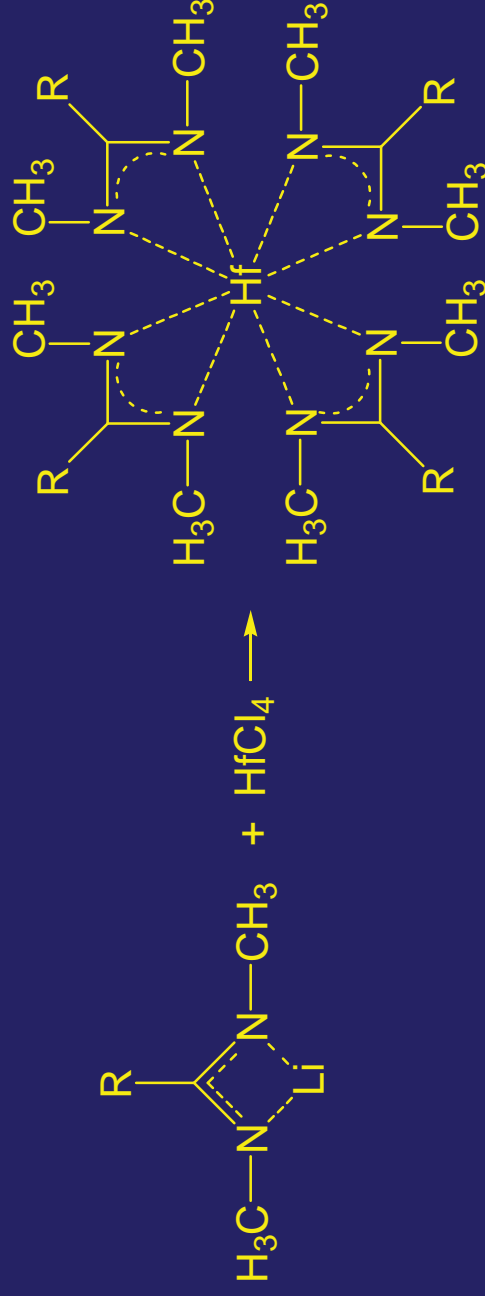
Known Hf precursors decompose too quickly
(except HfCl₄, which makes non-conformal films)

Synthesis of Hf Tetra-Amidates

Couple methylamine with an alkylnitrile and deprotonate with butyllithium:

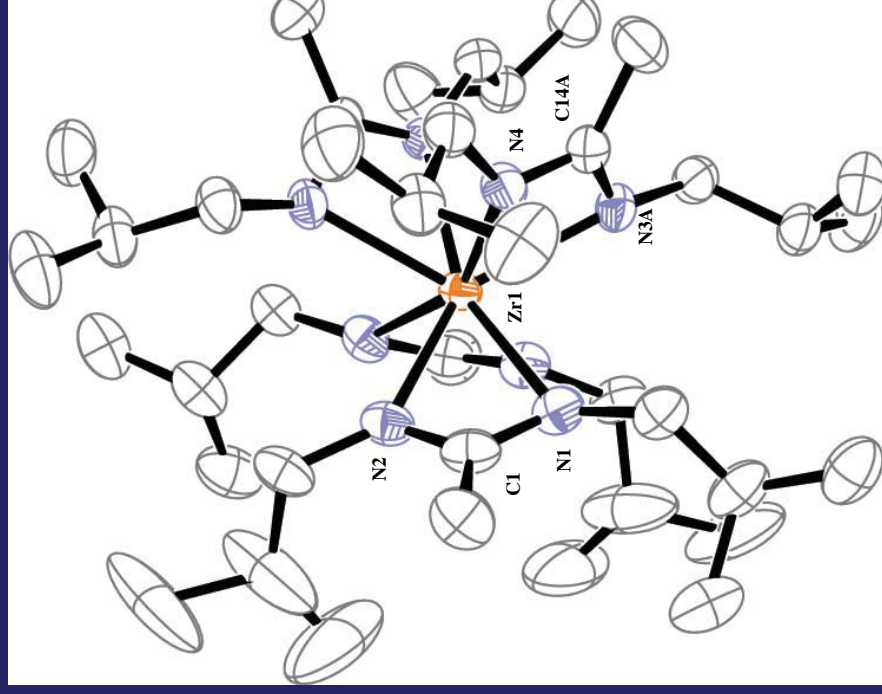


React the lithium salt with hafnium chloride:



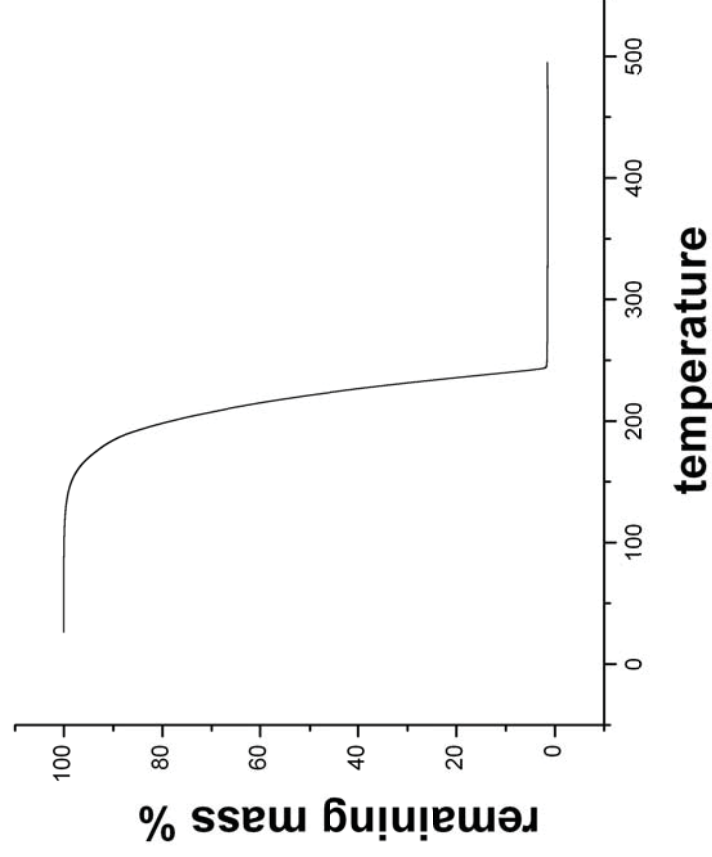
Structure of a Zr Tetra-Amidinate

X-ray structure of
 $\text{Zr}(\text{isobutyl}_2\text{-amd})_4$



Properties of a Hf Amidinate

TG data for
 $\text{Hf}(\text{Me}_2\text{-amd})_4$

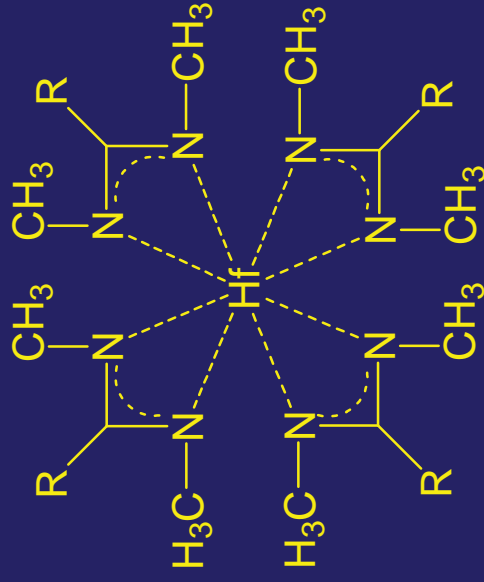


High thermal stability:

No residue after TG

No change of NMR after heating at 250 °C for 1 week

Melting Points of Hf Amidinates



R	Melting Point, °C	TG T _{1/2} °C
CH ₃	171	221
CH ₂ CH ₃	80	251
CH ₂ CH ₂ CH ₃	<20	246

⇒ R = CH₃ (methyl) is the most volatile;
soluble in hydrocarbons for use in direct liquid injection

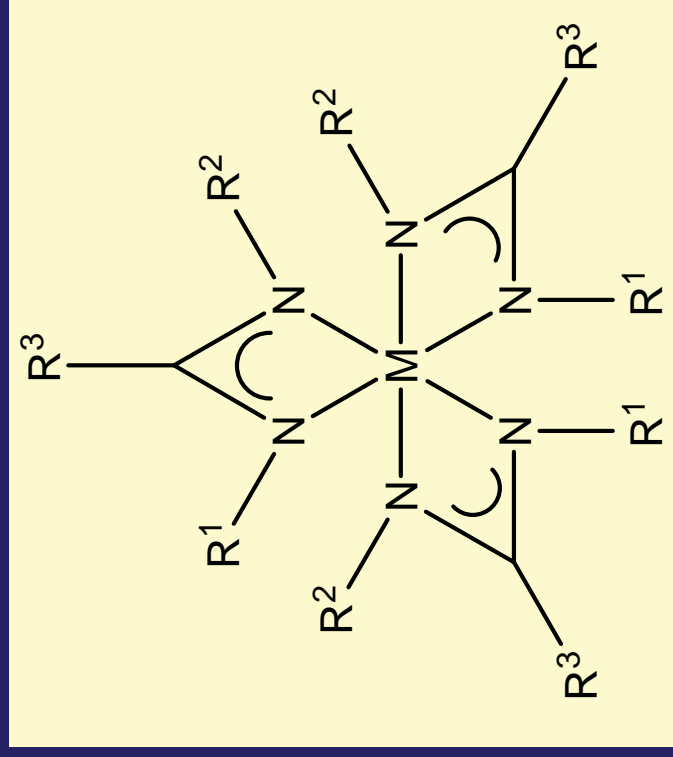
⇒ R = CH₂CH₂CH₃ (propyl) is a liquid Hf precursor

ALD experiments in progress

Advantages of GdScO_3 as high- κ dielectric

- High dielectric constant ($k \sim 22$) for amorphous phase
- Sharp interface with silicon, and no low- κ interlayer
- Stays amorphous and doesn't form alloys with Si or Ge after respective S/D activation processes
- Conduction band offset w.r.t Si is about 2-2.5eV, helping to achieve low leakage

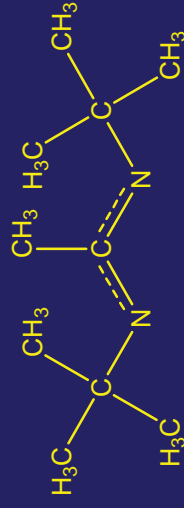
Amidinate Precursors for Gd and Sc



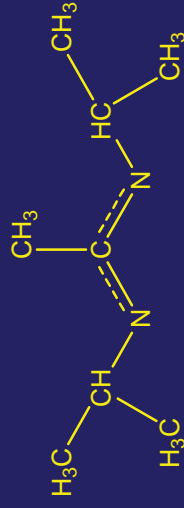
The Rⁿ are typically alkyl groups: ethyl, isopropyl, etc.

The choices of Rⁿ affect the reactivity, stability and volatility.

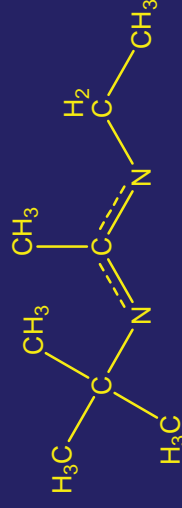
Some Amidinate Ligands



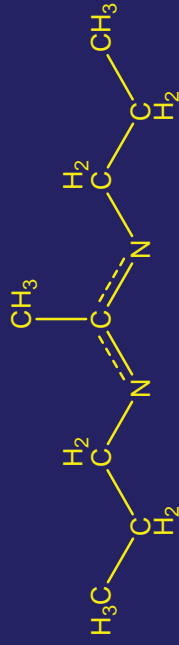
tert-butyl₂-acetamidinate



isopropyl₂-acetamidinate



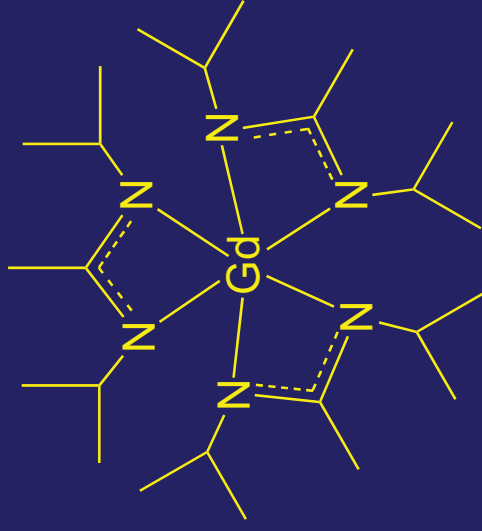
ethyl-*tert*-butyl-acetamidinate



n-propyl₂-acetamidinate

Increasing ligand bulk ↑

Precursors for GdScO_3 ALD

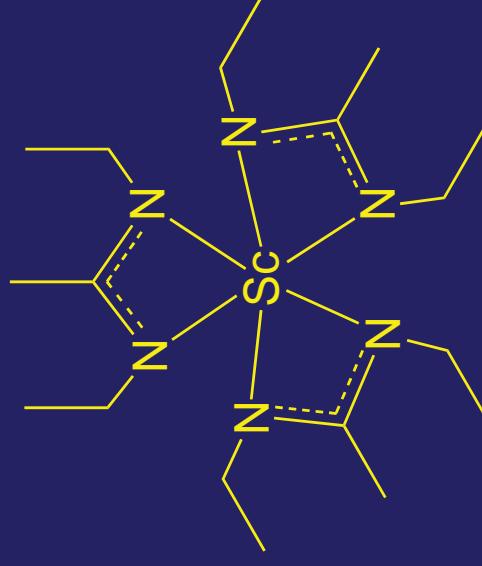


Tris-diisopropylacetamidinato
Gadolinium (III)



Bubbler Temp. : 140 °C

Thermally Stable to 320 °C



Tris-diethylacetamidinato
Scandium (III)



Bubbler Temp. : 140 °C

Thermally Stable to 350 °C

Both precursors nucleate well on hydrogenated Si surfaces and show high reactivity with H_2O .

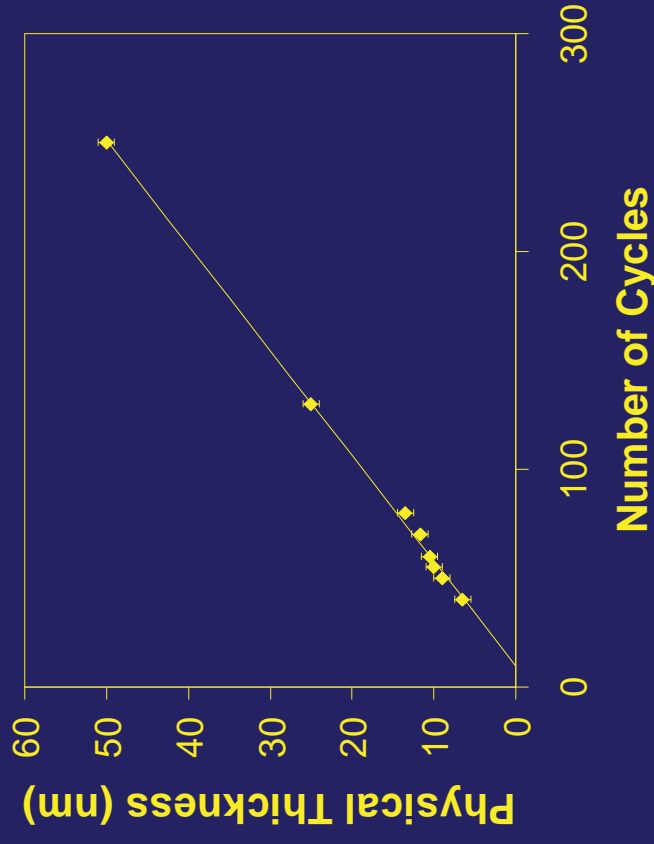
No need for chemical oxide interlayer to initiate growth

ALD of GdScO_3 films

1 cycle :

6s	Sc	Purge	H ₂ O	Purge	Gd	Purge	H ₂ O	Purge
		15s		4s		15s		4s
				40s				40s

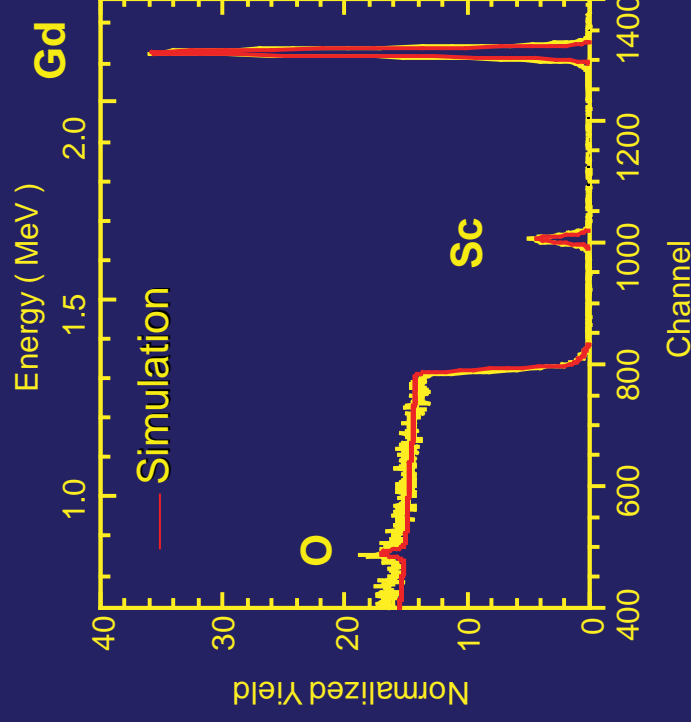
- Flow-through type tube reactor
- Substrate temperature 310 °C



- Linear growth rate ~ 2.0 Å/cycle
- Slight inhibition (~10 cycles) on HF-last Si

Structural Properties of GdScO_3 Films

- XRD and TEM indicate the films are **amorphous** as deposited and stay amorphous up to 950 °C



- Stoichiometry of a 25nm thick film by RBS: $\text{Gd}_1 \text{Sc}_{1.07} \text{O}_3$ with 1:1 dosing of Gd and Sc

Advantages of WN as a Gate Metal

- Excellent diffusion barrier for oxygen and metal atoms
- Can be etched easily
- Good thermal stability
- Good adhesion to oxides as well as metals

ALD of Tungsten Nitride, WN

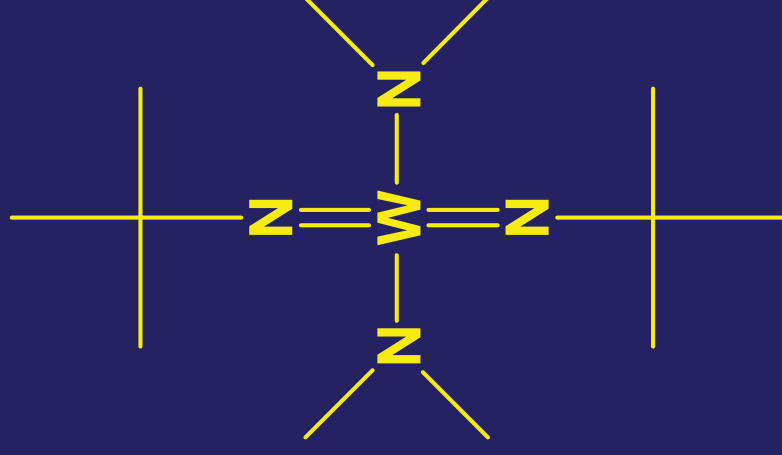
- Precursor :

Bis(*tert*-butylimido)bis-(dimethylamido)tungsten(VI)

Vapor pressure ~ 37 mTorr at 30 °C

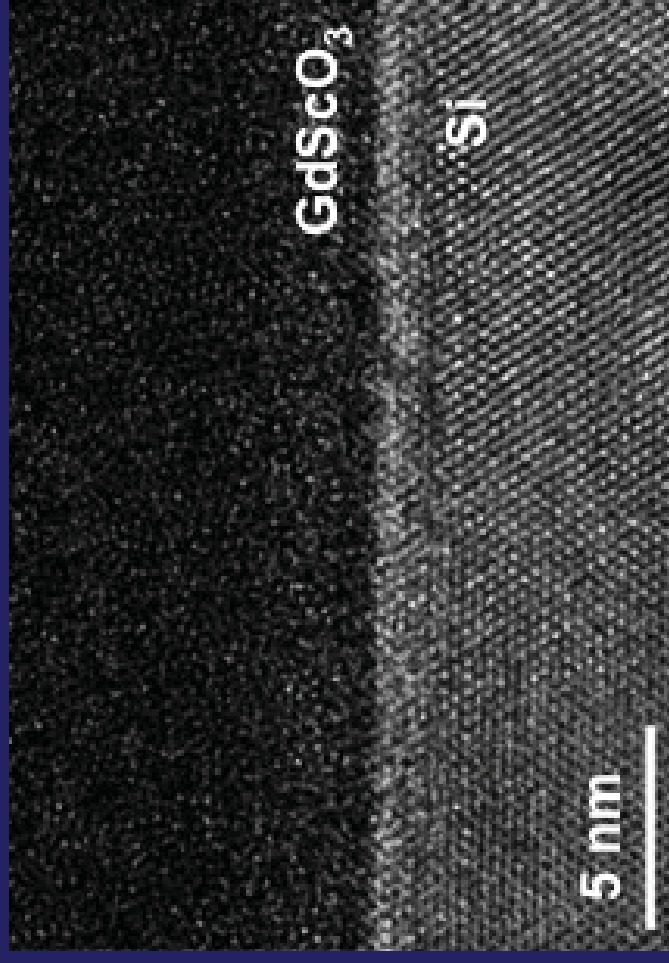
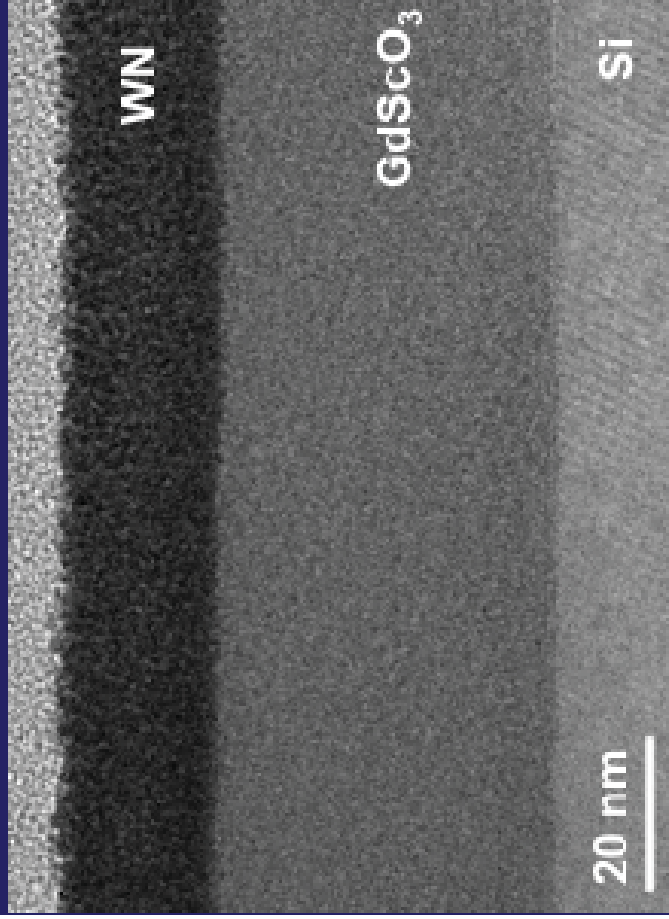
Liquid at room temperature

- NH₃ gas is used as a co-reactant
- Growth Rate at 385 °C ~ 2.0 Å / cycle
- Resistivity of W after anneal > 750 °C ~ 10⁻⁴ Ω-cm



J. S. Becker and R. G. Gordon, Appl. Phys. Lett. 82, 2239 (2003)

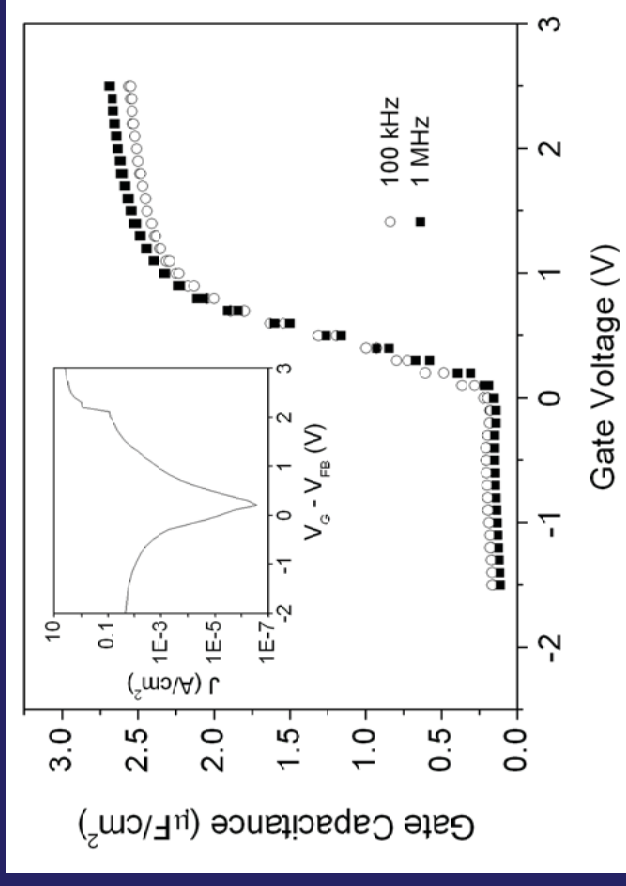
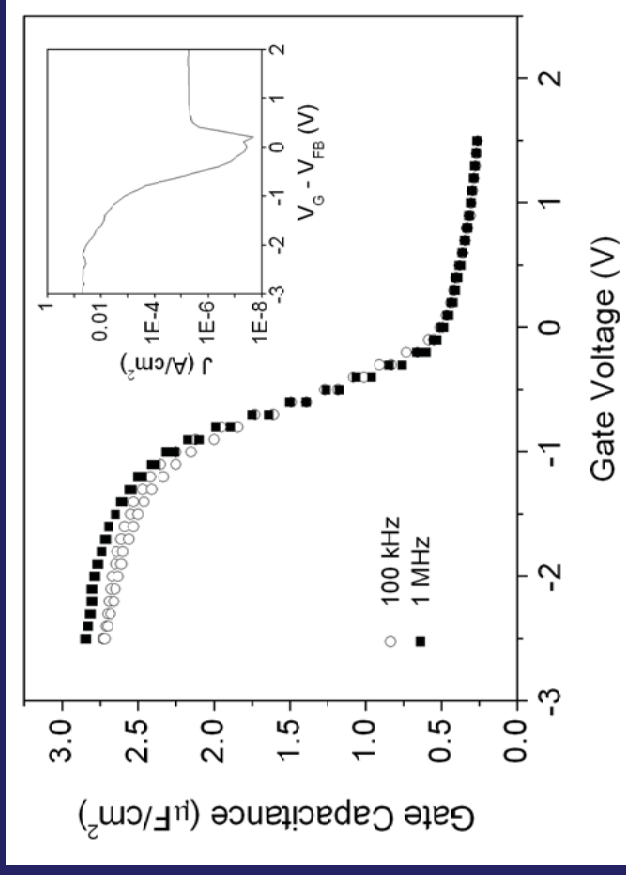
HRXTEM of a WN / GdScO₃ / Si Stack



- Films are very uniform over a wide area.
- GdScO₃ is amorphous as deposited.
- Interfaces are sharp and smooth.

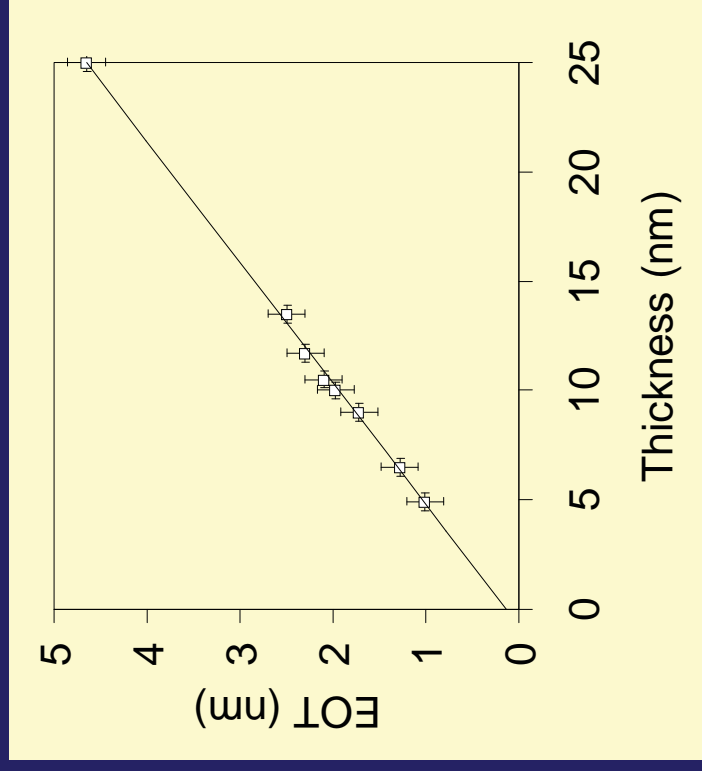
Electrical Properties of a Low EOT Film

WN / GdScO₃ / Si (100) Capacitors



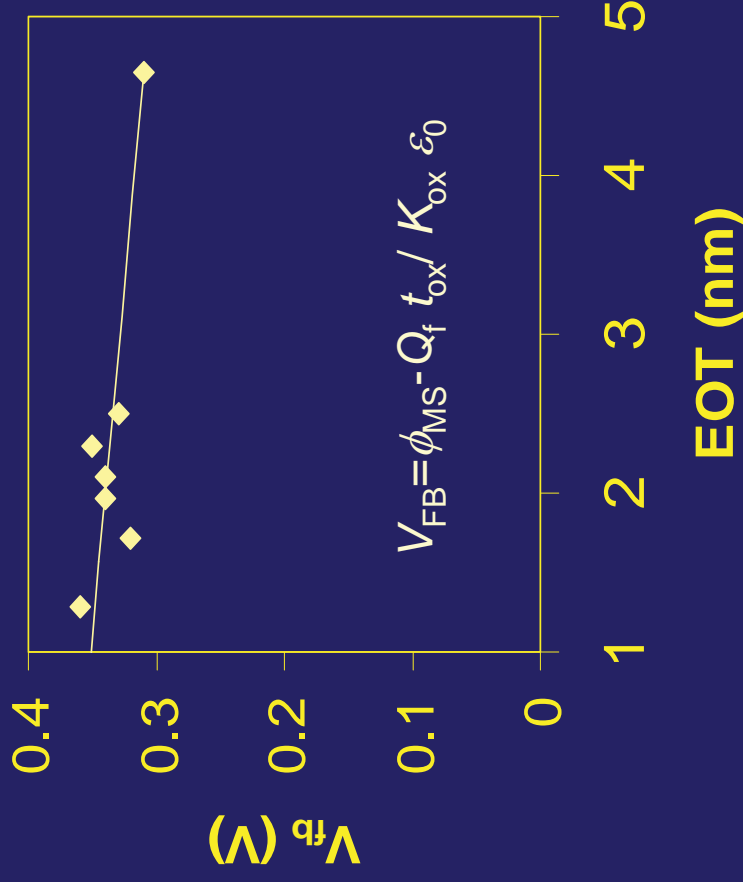
- EOT ~ 1 nm
- Flatband Voltage $\sim +0.35$ V
- Hysteresis $\Delta V < 60$ mV
- Leakage Current density at 1V $\sim 10^{-3}$ A/cm²

Equivalent Oxide Thickness vs. Physical Thickness



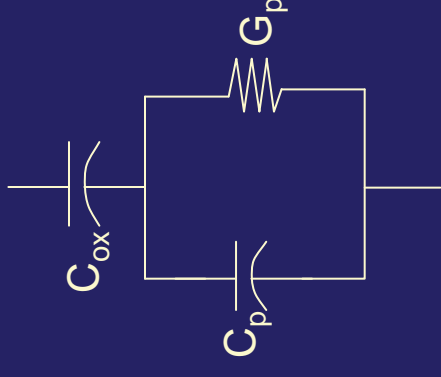
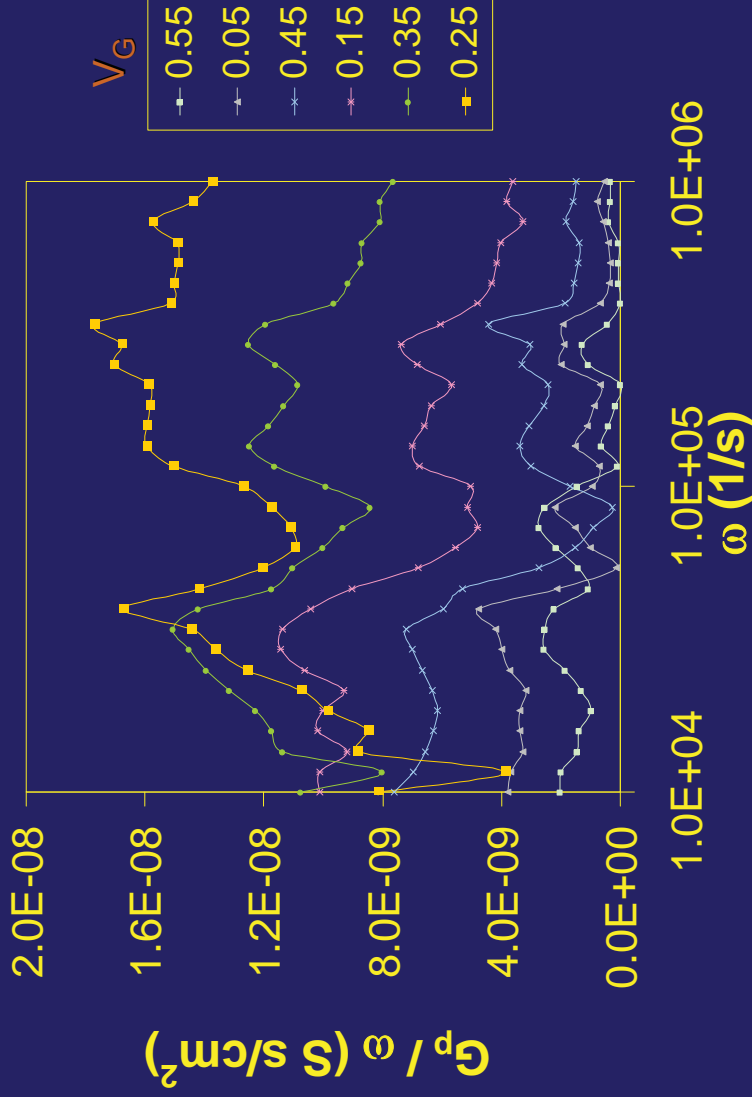
$\kappa \sim 21.5$, interfacial layer $\sim 1.5 \text{ \AA}$

Fixed Charge Density and Work Function of WN



- Fixed Charge Density $\sim 2.4 \times 10^{11} \text{ cm}^{-2}$
- Work Function of WN $\sim 4.6 \text{ eV}$ (midgap metal)

Interface Trap Density by Conductance Method

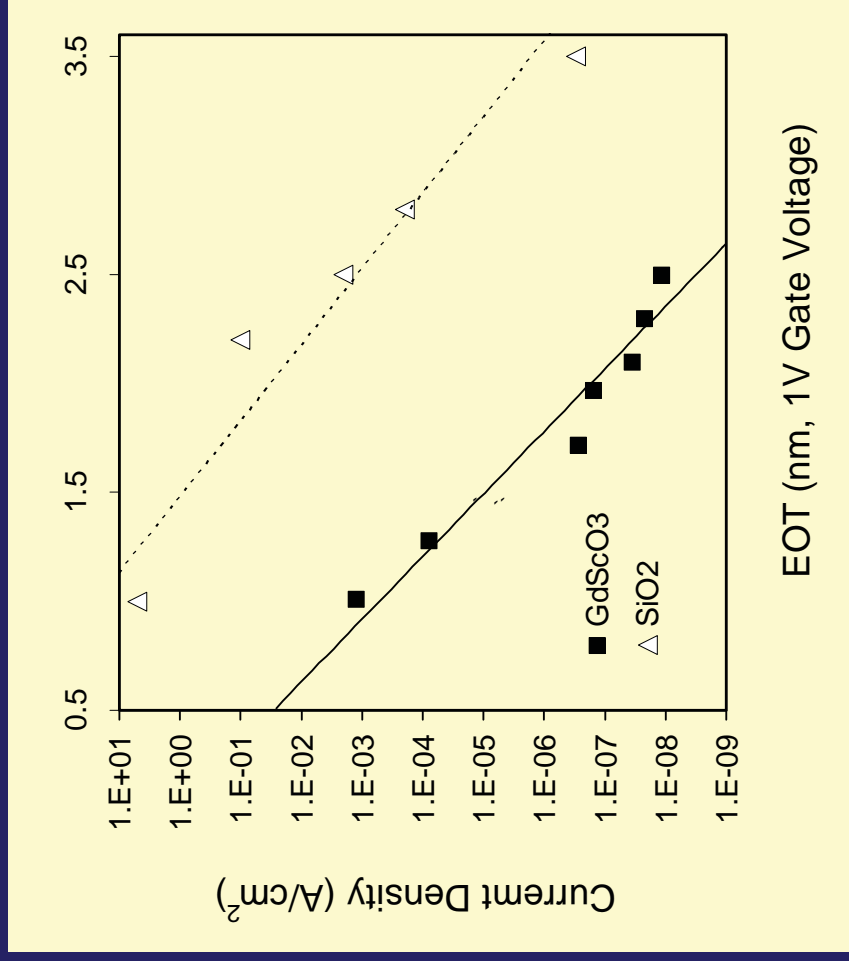


Equivalent circuit
for the Gate Capacitor

$$D_{it} \approx \frac{2.5}{q} \left(\frac{G_p}{\omega} \right)_{\max}$$

- Interface Trap Density $\sim 2.8 \times 10^{11} \text{ cm}^{-2} \text{ eV}^{-1}$

Leakage Current vs. EOT



- leakage current $\sim 10^{-3}$ A cm⁻² at EOT = 1 nm
- leakage current $\sim 10^{-7}$ A cm⁻² at EOT = 2 nm
- $\sim 10^4$ times lower than SiO₂

Summary of GdScO₃

- Ultrathin amorphous GdScO₃ films were deposited on HF-last Si substrates by ALD using Gd and Sc amidinate precursors.
- ALD WN / GdScO₃ gate stacks show promising electrical properties for future MOSFET and DRAM applications
 - **High κ (~ 21.5) and Low EOT (~ 1 nm)**
 - **Low leakage current ($< 10^{-3}$ A/cm² at 1V for EOT ~ 1 nm)**
 - **Reasonably low fixed charge and interface trap density**

Outline of Cu / Ru / WN for Interconnects

**Ru(amidinate) precursor
structure
deposition conditions**

**Properties of Ruthenium films
conformal and complete coverage
smooth surface
low resistance
strong adhesion to WN**

**Copper seed layers
conformal and complete coverage
low resistance
electrochemical deposition of Cu
survive CMP of Cu**

Motivation for using Ruthenium or Cobalt instead of Tantalum as an adhesion layer

Tantalum has low adhesion to CVD or ALD copper

Ta, Ru and Co all have these required properties:

- Low solubility in copper
- Strong interfacial adhesion to copper

Ta $>6 \text{ J m}^{-2}$

Ru $\sim 4 \text{ J m}^{-2}$

Co $>30 \text{ J m}^{-2}$

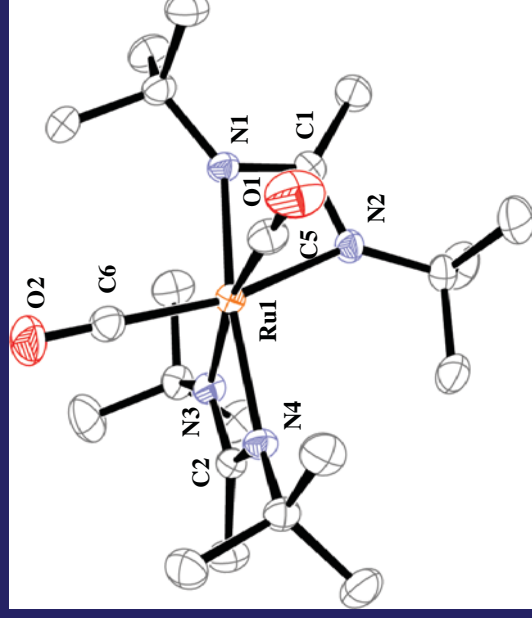
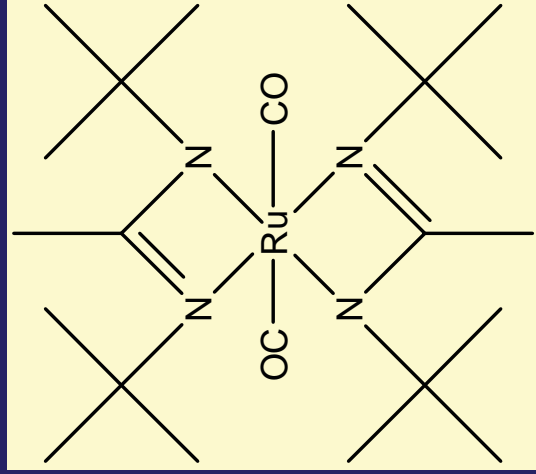
- Low electrical resistivity

Ta $13.4 \text{ } \mu\Omega\text{-cm}$

Ru $7.4 \text{ } \mu\Omega\text{-cm}$

Co $5.8 \text{ } \mu\Omega\text{-cm}$

Ru Precursor $\text{Ru}(\text{tBu-amd})_2(\text{CO})_2$



Chemical formula

Molecular structure by
x-ray crystallography

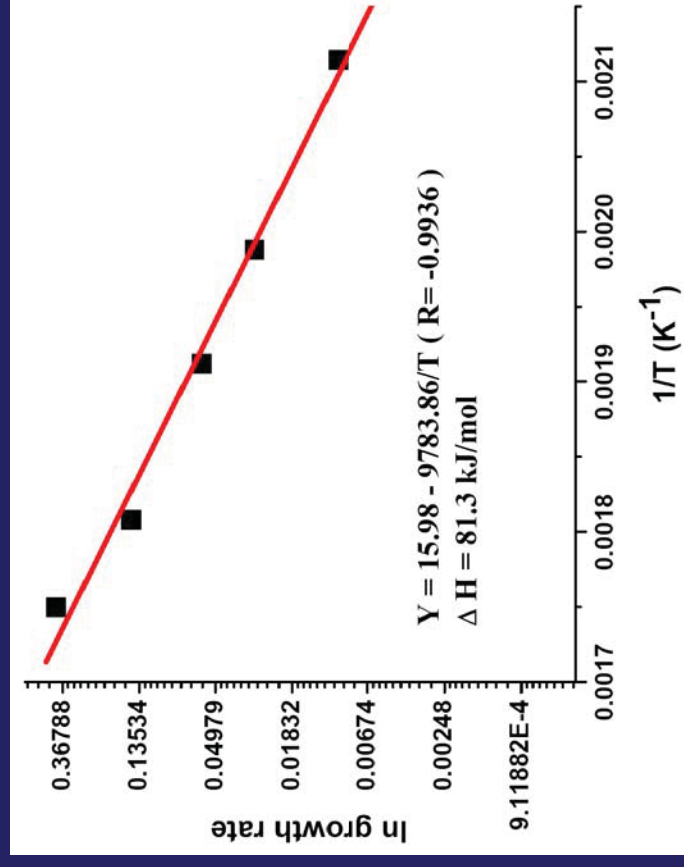
- vapor pressure >0.05 Torr at 130 °C
- low evaporation residue 0.14% by TG
- air and moisture stable

ALD/CVD of Ru

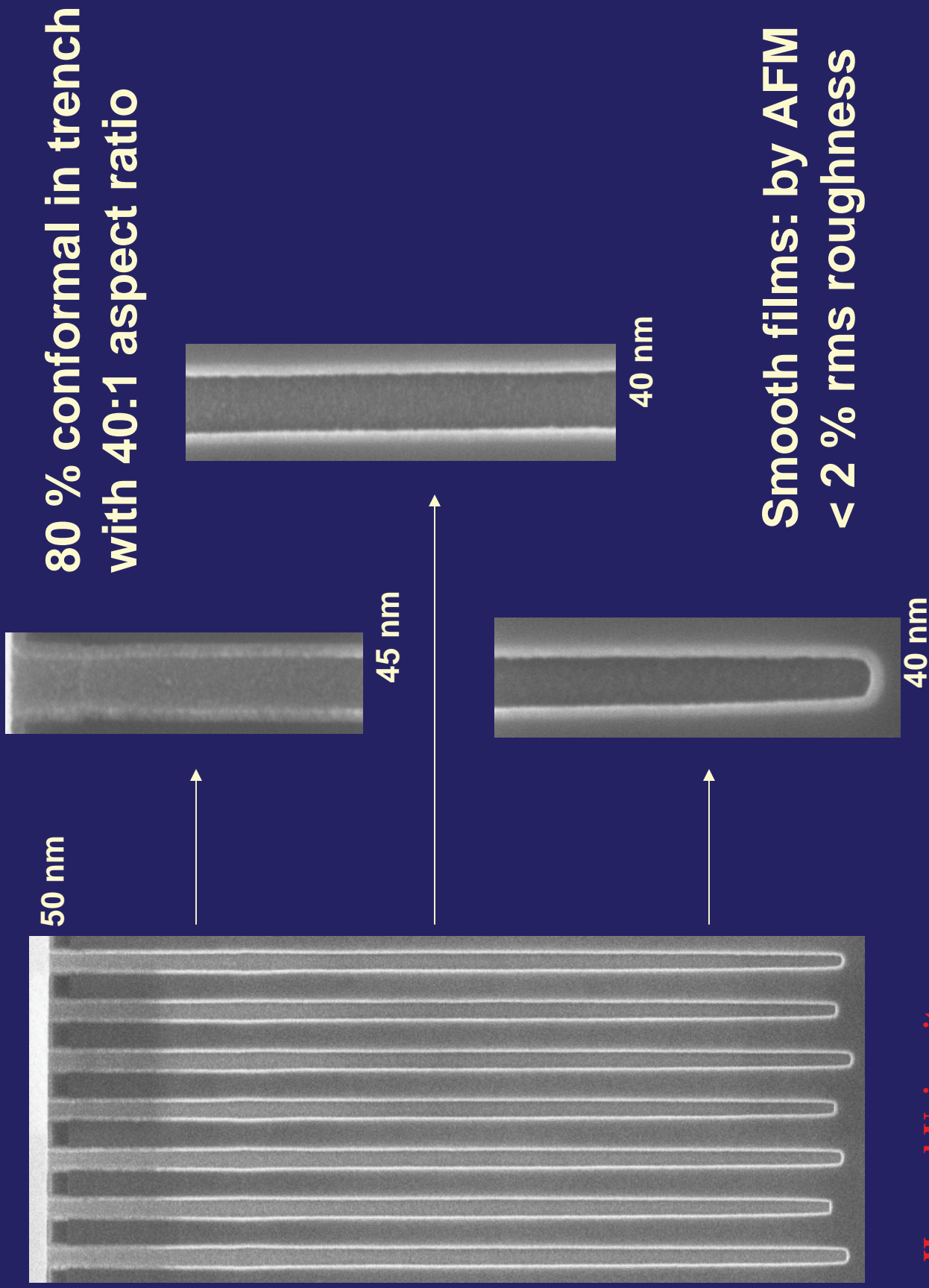
Conditions:

- Bubbler: 130 °C
- Substrate: 200 - 300 °C
- co-reactant: NH₃
- Pressure: 1 ~ 2 Torr
- ~1.5 Angstrom/cycle at 300 °C
- No saturation of growth/cycle

=> Mostly surface controlled CVD, only partly ALD

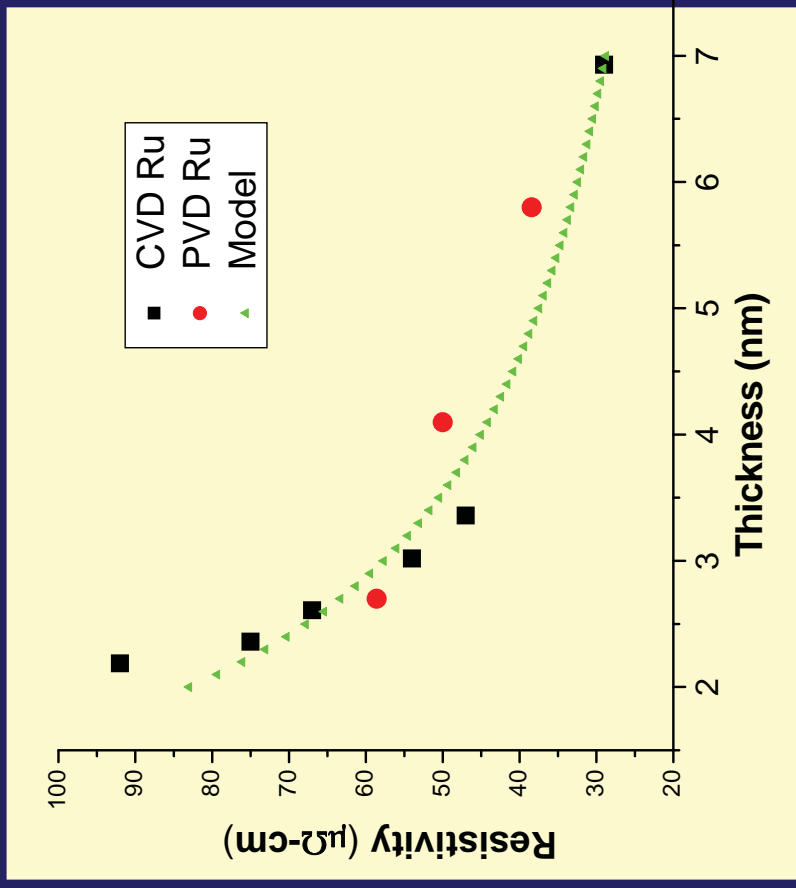


High Conformality of ALD/CVD Ru/WN/SiO₂



Electrical Conductivity of CVD Ru / SiO₂

Conductivity matches pure Ru films sputtered onto Ta



Grain Boundary Scattering Model

$$\rho/\rho_0 = 1 + 1.5\{R/(1-R)\} (\lambda / g)$$

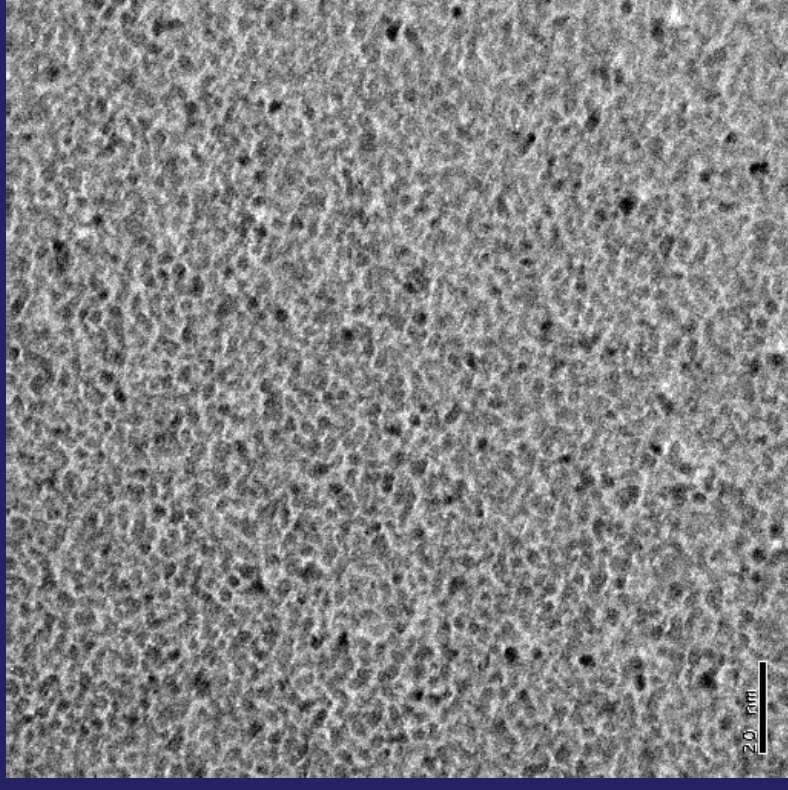
where the average grain size g is estimated as the film thickness scattering coefficient for electrons at grain boundaries $R = 0.58$

bulk scattering length $\lambda = 10$ nm

bulk resistivity $\rho_0 = 7.1 \mu\Omega\text{-cm}$

2 nm ALD/CVD Ru completely covers WN

- no pinholes seen by vertical TEM:
- an etch solution $\text{H}_2\text{O}_2/\text{NH}_4\text{OH}$, which normally dissolves a WN film within one minute, does not etch the WN under a 2 nm Ru film even after one hour.



- Ru films are strongly adherent to freshly-deposited WN
adhesion energy $\sim 4 \text{ J/m}^2$ by 4-point bend test.

Nucleation of Cu(3nm) on Ru(2nm) / WN(3nm) / Si₃N₄



Vertical TEM shows complete coverage

Chemical test for completeness of coverage by copper:

Ru catalyzes H₂O₂ decomposition rapidly forming bubbles of oxygen

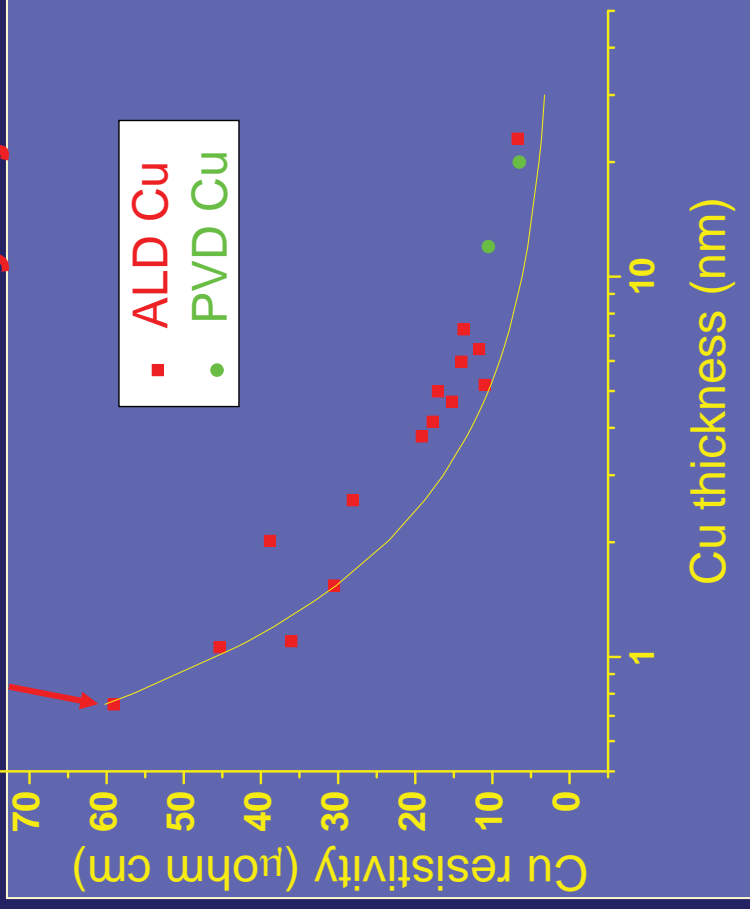
Cu bubbles very slowly

Cu / Ru also bubbles very slowly

Zhengwen Li and Roy G. Gordon, *Chem. Vap. Deposition* 2006, 12, 435–441

Electrical Resistance of ALD Cu on Ru

Continuous layer just 3 Cu atoms thick!



Grain Boundary Scattering Model

$$\rho/\rho_0 = 1 + 1.5\{R/(1-R)\} (\lambda / g) \quad \text{---}$$

where the average grain size g is estimated as the film thickness

scattering coefficient for electrons at grain boundaries $R = 0.3$

bulk scattering length $\lambda = 39 \text{ nm}$

bulk resistivity $\rho_0 = 1.7 \mu\Omega\text{-cm}$

ALD Cu resistivity matches pure sputtered Cu films^a

(a) Rosnagel, S.; Kuan, T.; *J. Vac. Sci. Tech. B* **22**, 240 (2004)

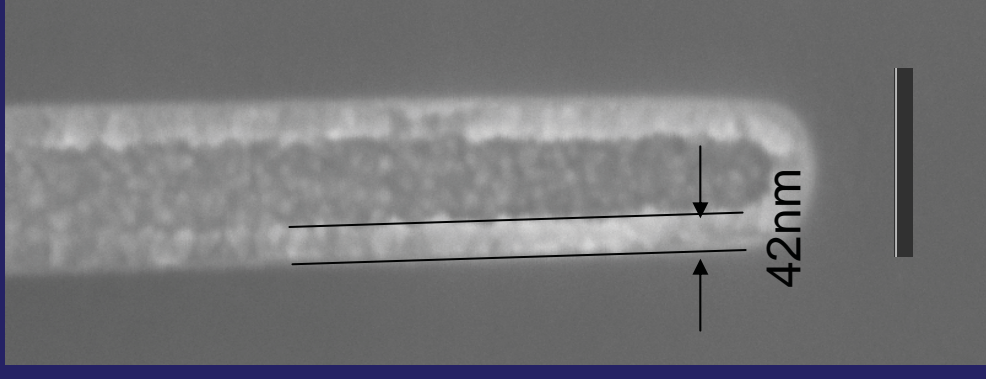
Harvard University

Conformality and Sheet Resistance of Cu / Ru / WN

SEM of copper film at bottom of hole with aspect ratio 40:1 and > 90% step coverage

Cu(3nm)/Ru(2nm)/WN(2nm) has sheet resistance ~ 50 Ω /sq

Suitable for electroplating



Cross Section of Electroplated Cu/Ru/WN/SiO₂

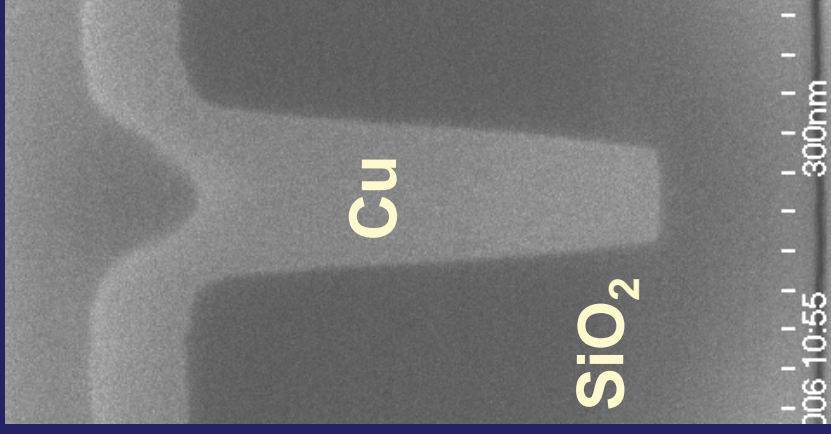
Electroplated by Tom P. Moffat

CMP by Christian Witt

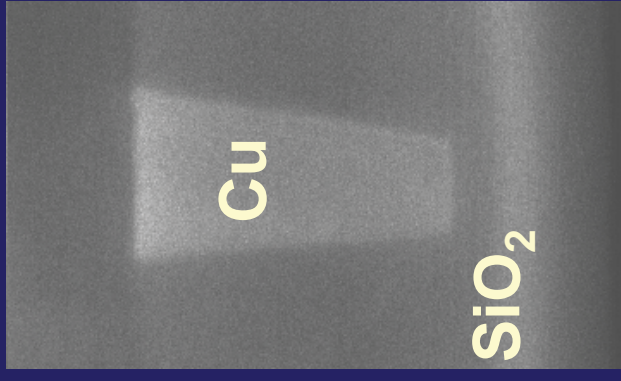
SEM by Daniel Josell, NIST

=> Adhesion strong
enough to survive CMP

Before CMP



After CMP



Interconnect Summary

ALD of SiO₂ pore sealant / WN barrier / Ru glue / Cu seed

- Extendable to the end of the roadmap
- No deposition on chamber walls => no chamber cleaning
- No plasma damage to substrates
- Simplified equipment design because no plasma needed

Acknowledgements

Metals: Booyong Lim, Antti Rahtu, Jin-Seong Park

Nickel: P. Venkateswara Rao, Kyoung-ha Kim

Copper, Cobalt: Zhengwen Li, Sean Barry, Don Keun Lee

Ruthenium: Huazhi Li, Titta Aaltonen

Metal Nitrides: Jill Becker, Seigi Suh, Esther Kim, Kyoung-ha Kim

Metal oxides: Dennis Hausmann, Philippe de Rouffignac, Jin-Seong Park,
Kyoung-ha Kim, Leo Rodriguez, Mike Coulter

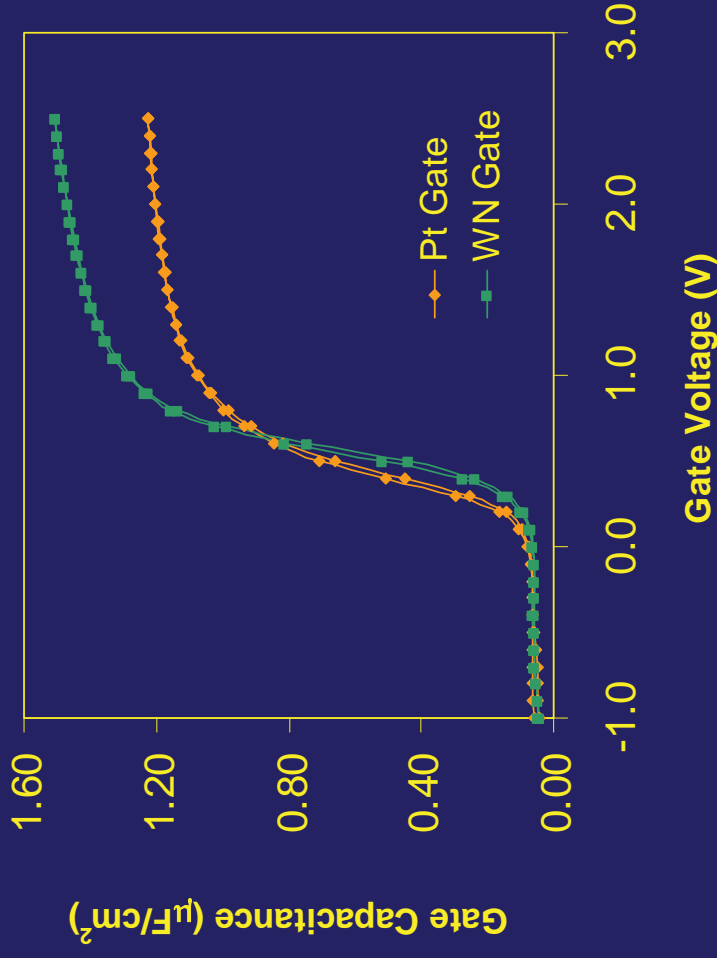
TEM: Damon Farmer; SEMATECH

Adhesion measurements: Joost Vlassak, Youbo Lin

DRAM trenches supplied by Infineon

Supported in part by the US National Science Foundation

In-situ Deposited WN Gate vs. Ex-situ Deposited Pt Gate



- *In-situ* WN gate stack shows 18% increase in capacitance